

FIG. 1A

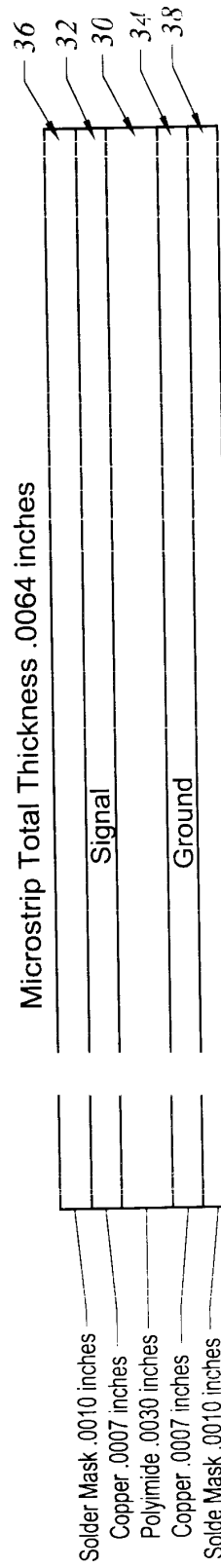
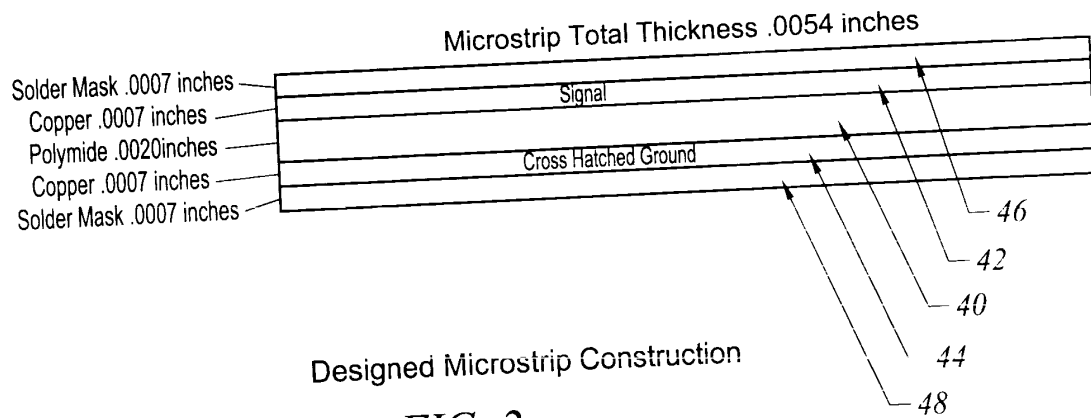
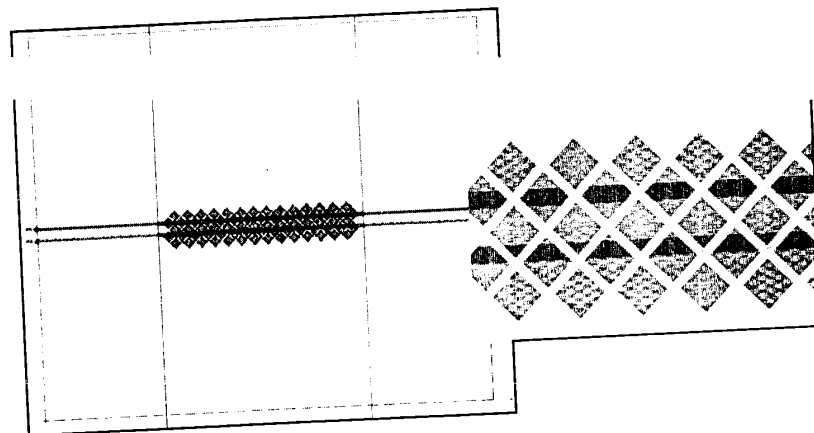


FIG. 1B



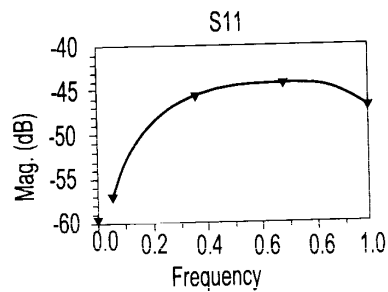
Designed Microstrip Construction

FIG. 2



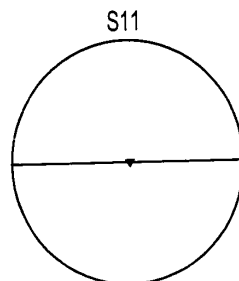
Simulation Set Up for 50 ohm Transmission Line

FIG. 3



Simulation Results for 50 ohm Transmission Line-
Amount of Reflection with a Transmission Line Terminated with 50 ohms

FIG. 4

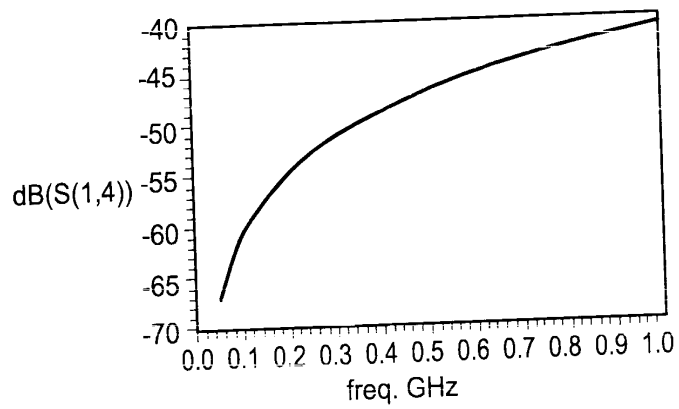


freq (50.00MHz to 1.000GHz)

Simulation Results for 50 ohms Transmission Line-
Amount of Reflection with a Transmission Line 50 ohms Plotted on a Smith Chart

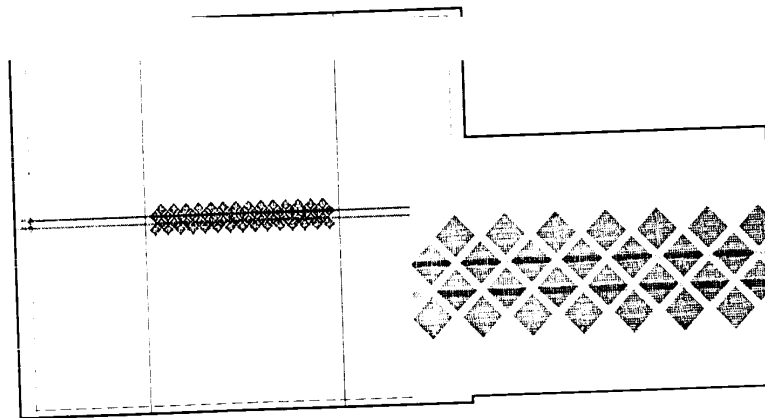
FIG. 5

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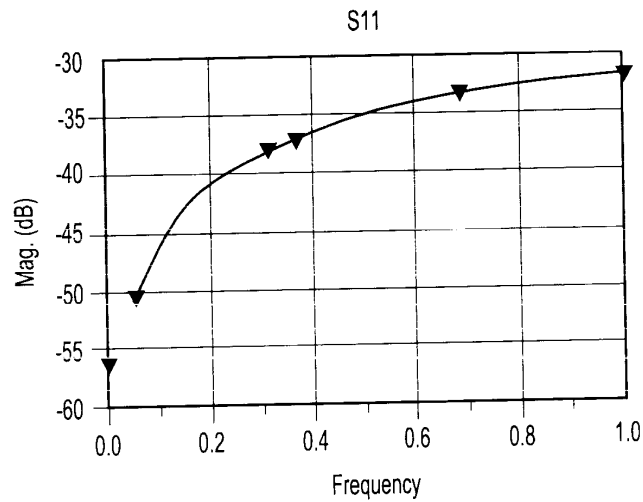
Simulation Results for 50 ohm Transmission line-Isolation
Between adjacent Traces

FIG. 6



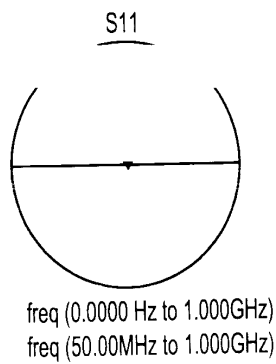
75 ohm Transmission Line Simulation Set Up

FIG. 7



Simulation Results for 75 ohm Transmission Line-
Amount of Reflection with a Transmission Line Terminated with 75 ohms

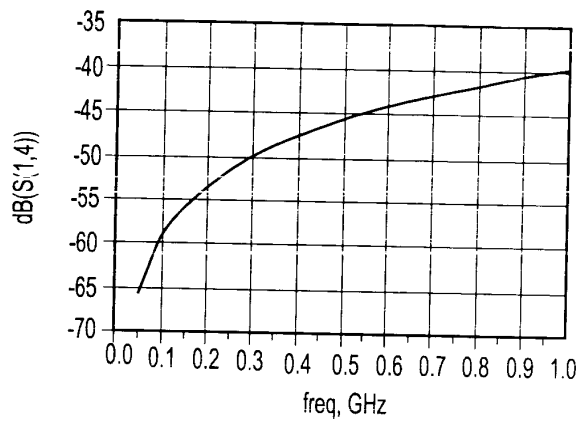
FIG. 8



Simulation Results for 75 ohms Transmission Line-
Amount of Reflection with a Transmission Line 75 ohms Plotted on a Smith Chart

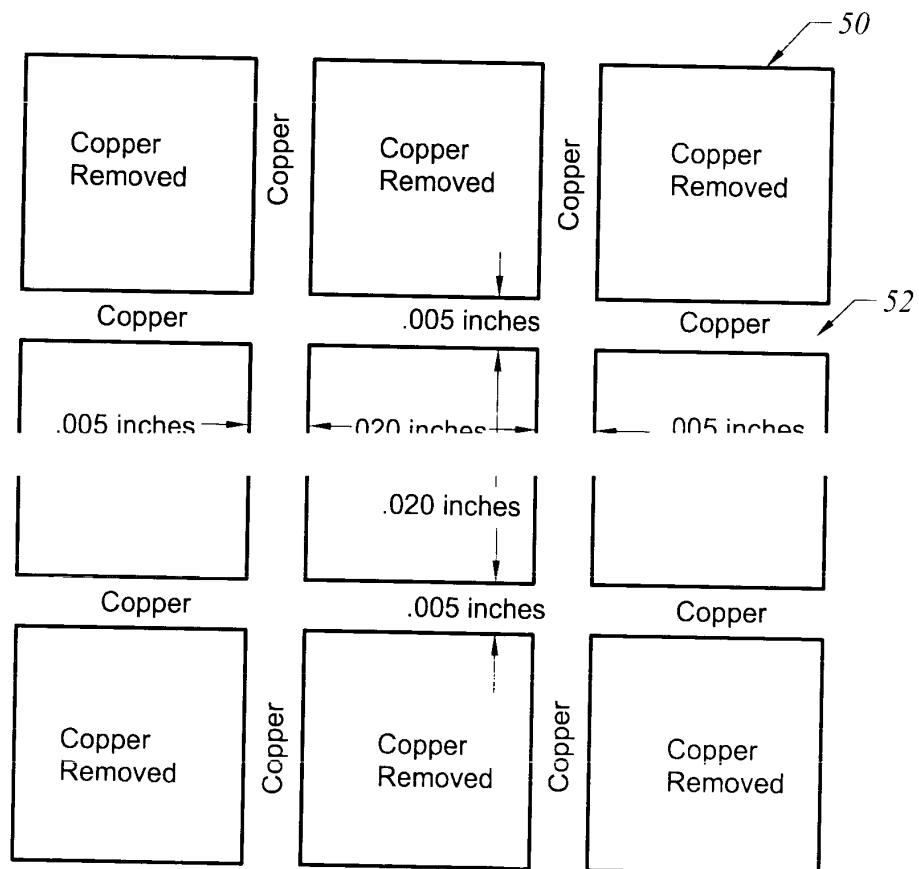
FIG. 9

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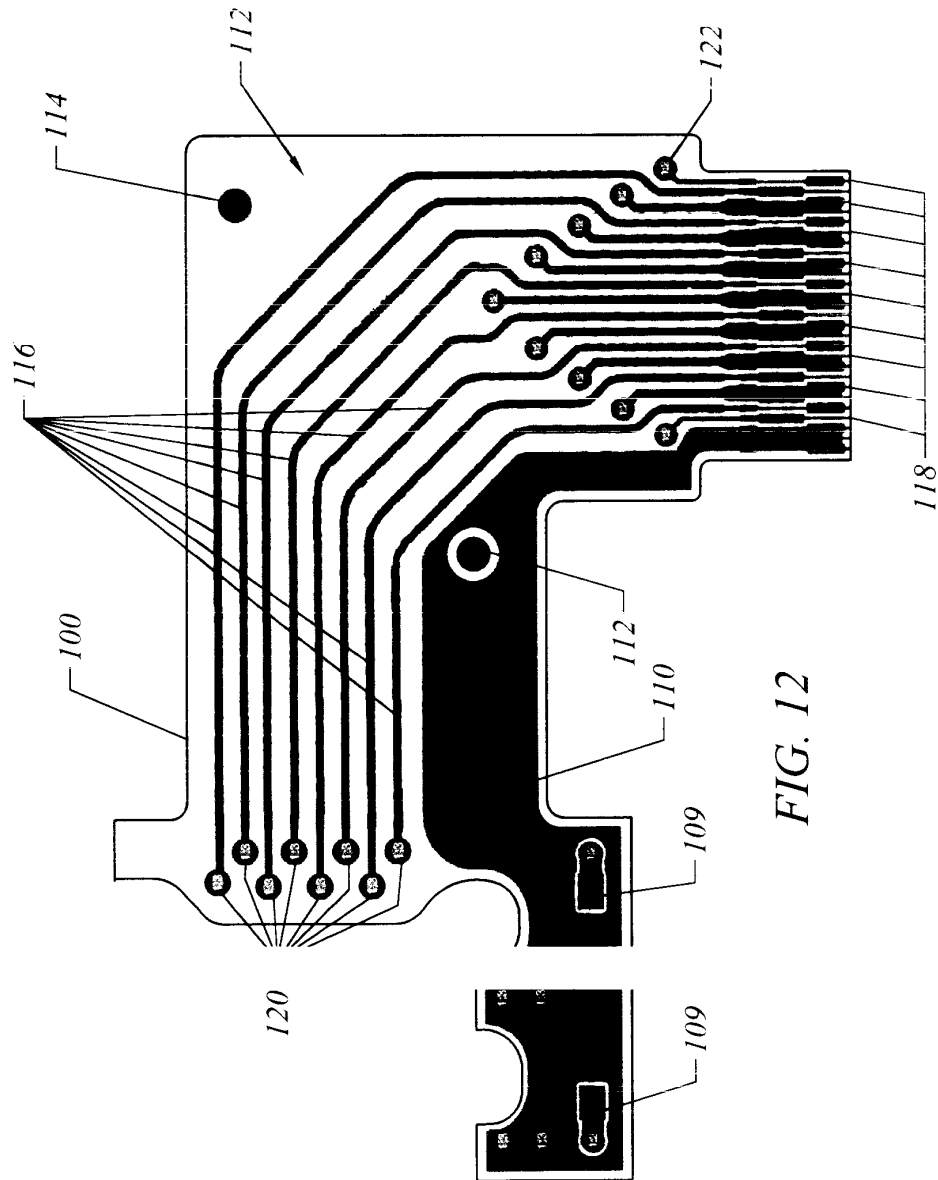
Simulation Results for 75 ohm Transmission Line - Isolation
Between Traces

FIG. 10



Cross Hatch Utilized on Ground Plane

FIG. 11



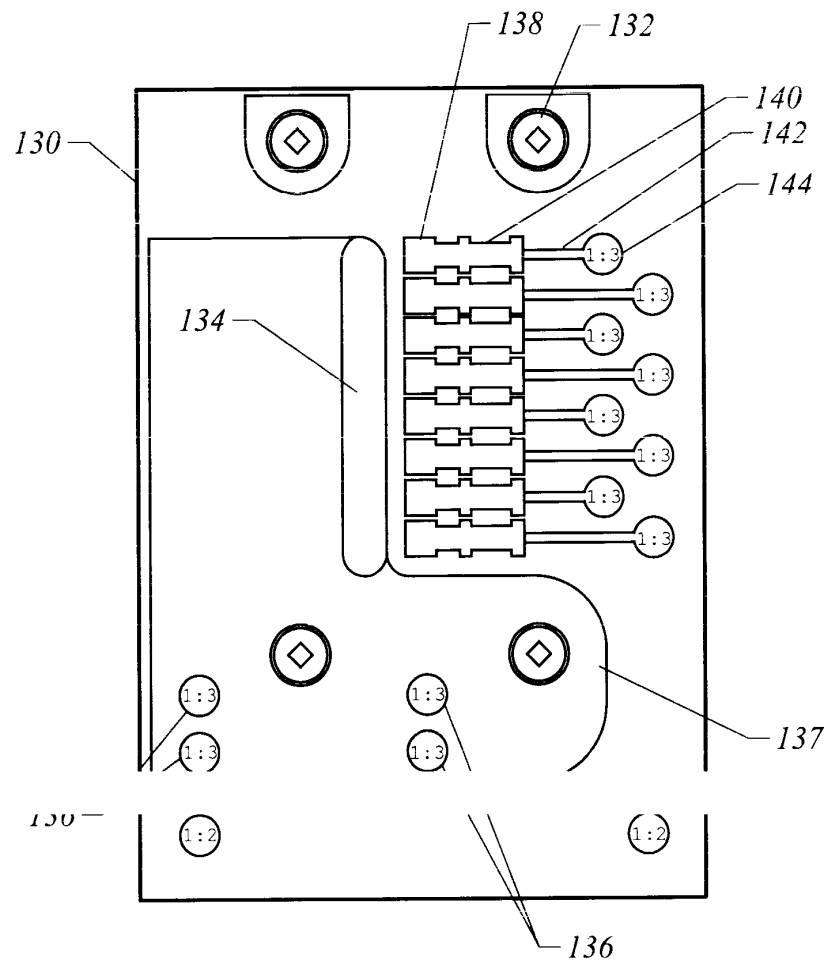


FIG. 13

FIG. 14A is a schematic diagram of a circuit board 100, showing a top view. The circuit board 100 is a rectangular substrate with a central cutout. A series of conductive traces 130 are routed along the top edge of the board, connecting to a series of pads 120. The pads 120 are arranged in a row along the top edge of the board. The traces 130 are routed in a curved path, connecting the pads 120 to a series of pads 110 located on the right side of the board. The pads 110 are arranged in a row along the right edge of the board. The circuit board 100 also includes a series of pads 140 located on the bottom edge of the board. The pads 140 are arranged in a row along the bottom edge of the board. The circuit board 100 is shown with a top view, and the bottom view is shown in FIG. 14B.

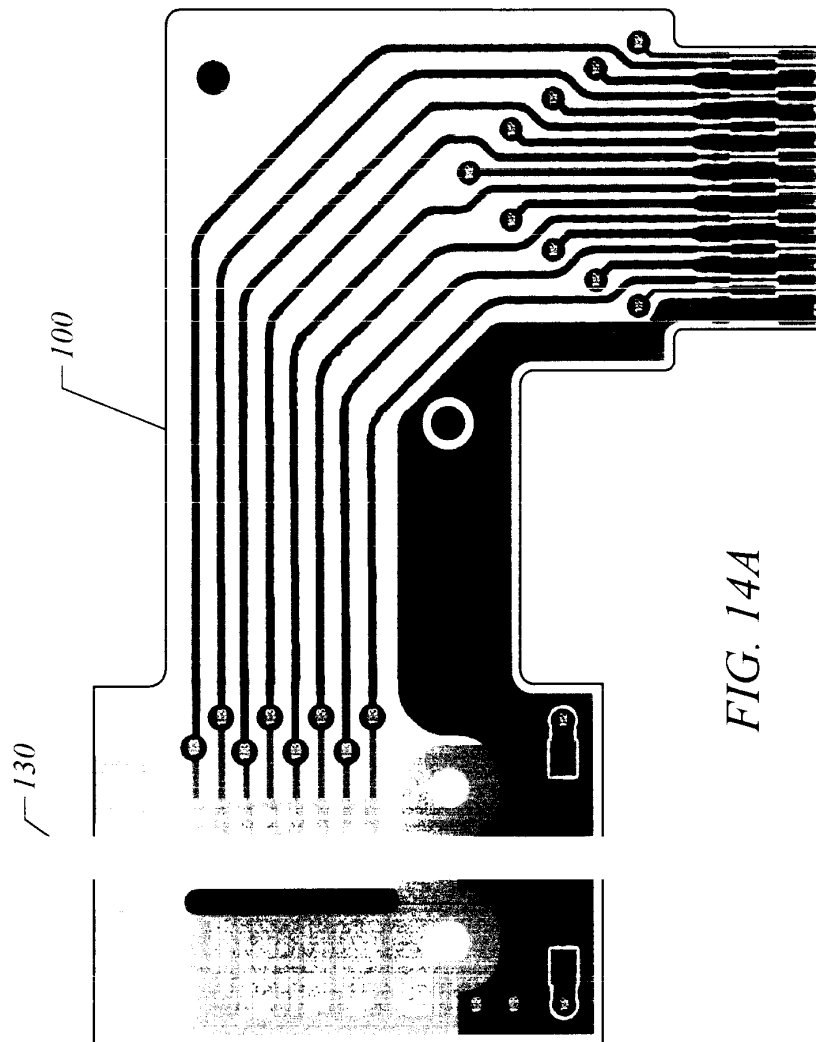


FIG. 14A

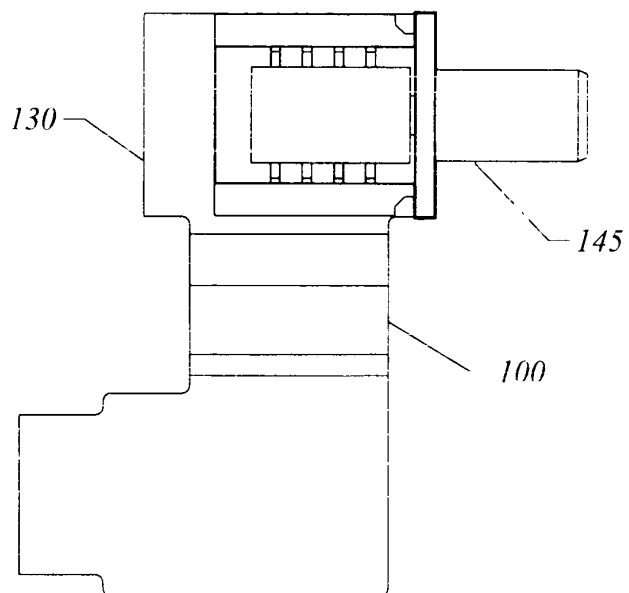


FIG. 14B

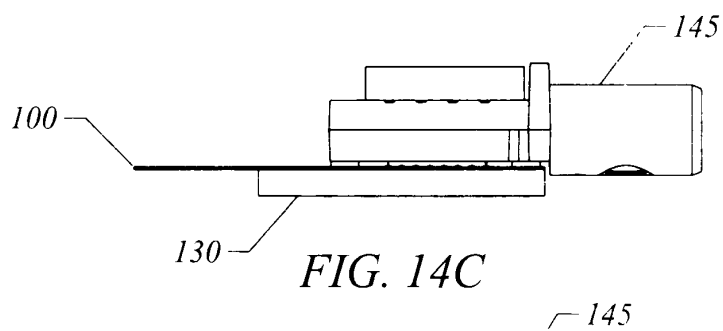


FIG. 14C

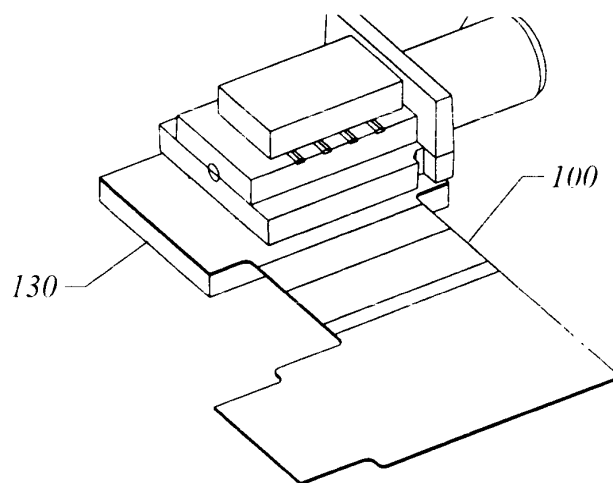


FIG. 14D

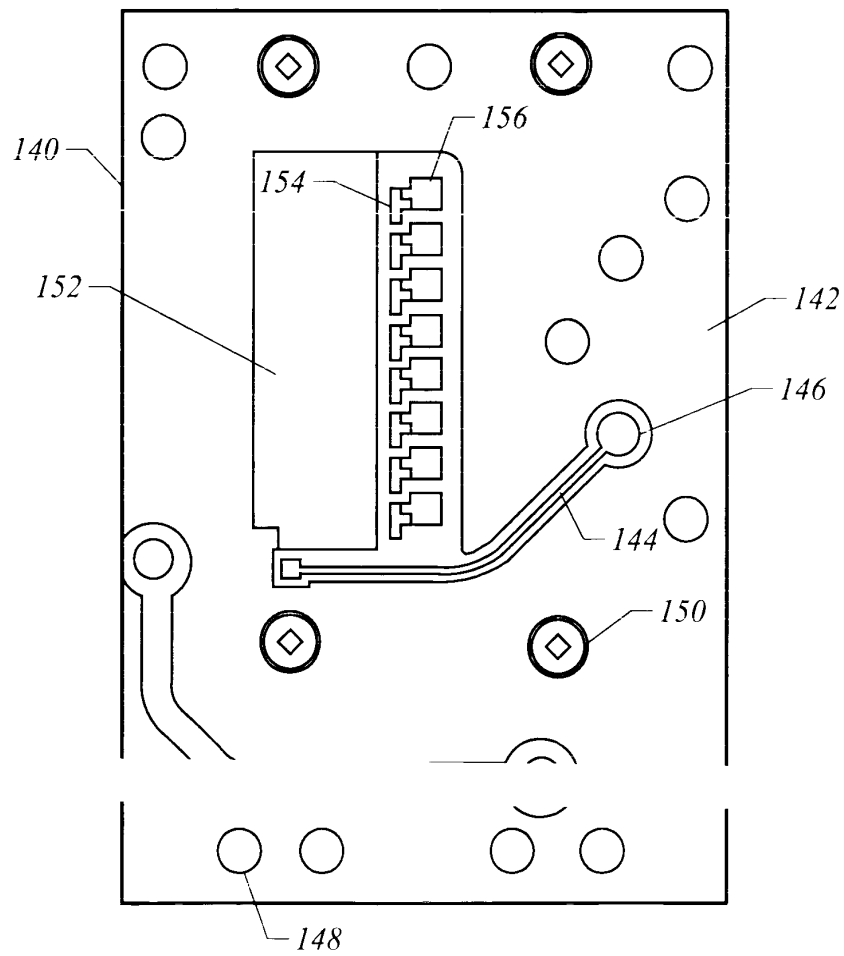


FIG. 15

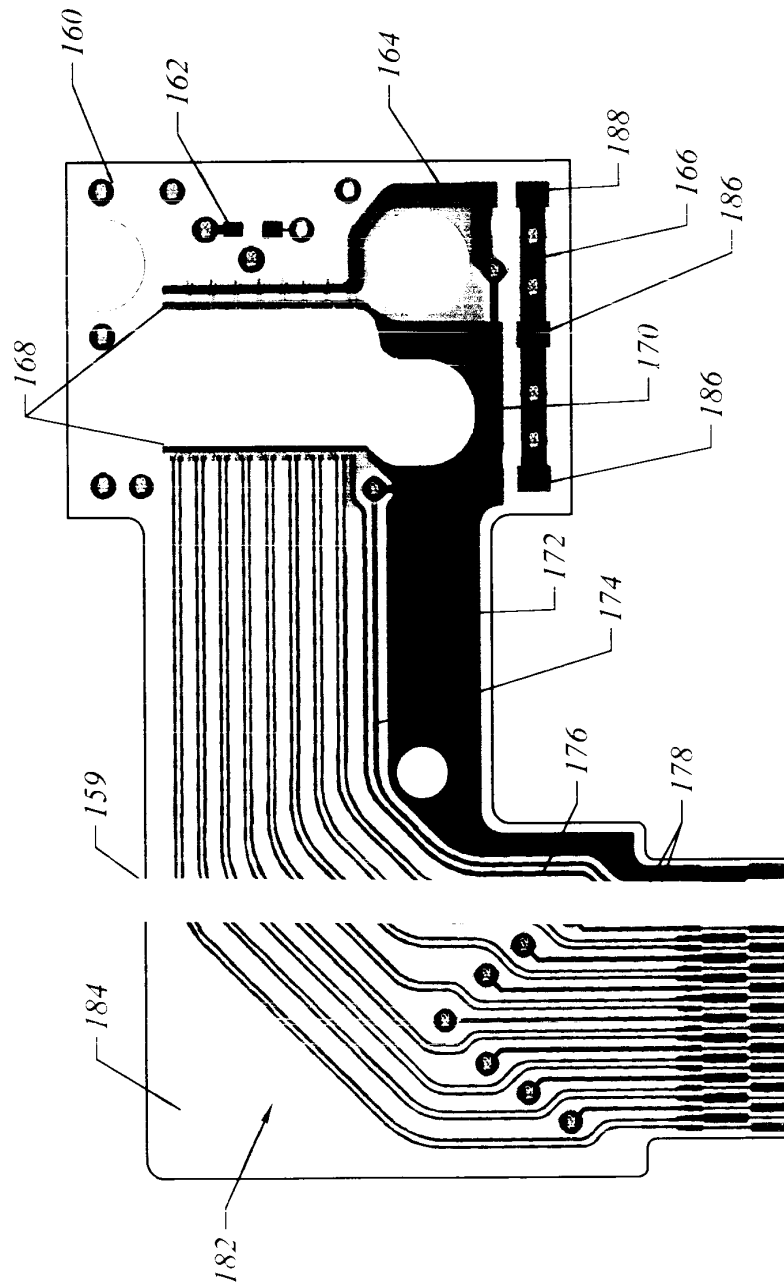


FIG. 16

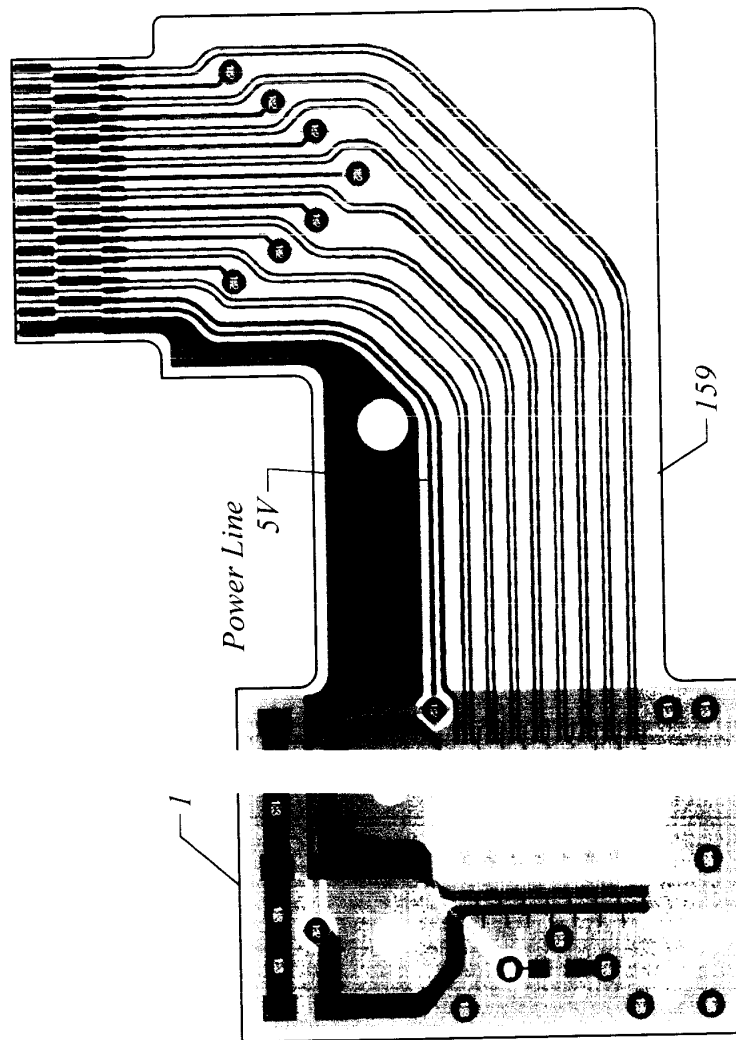


FIG. 17A

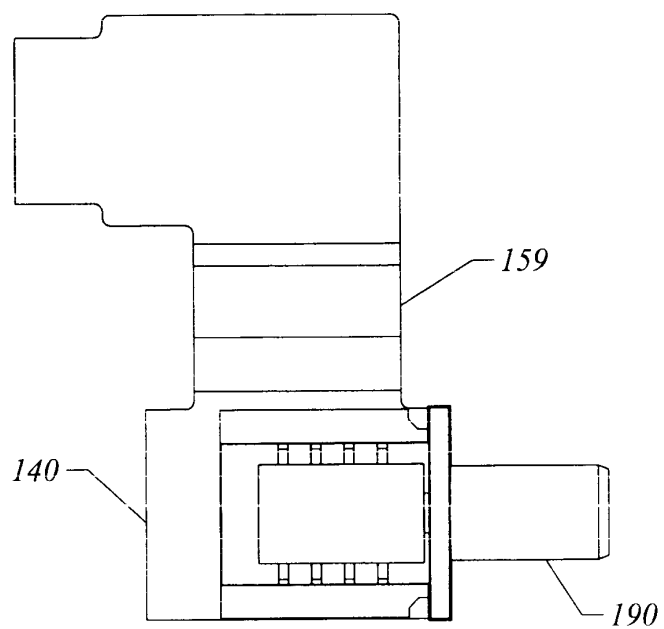


FIG. 17B

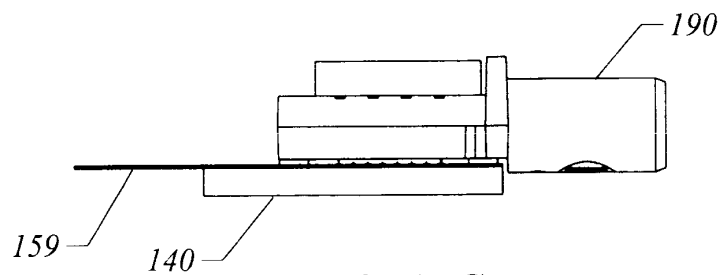


FIG. 17C

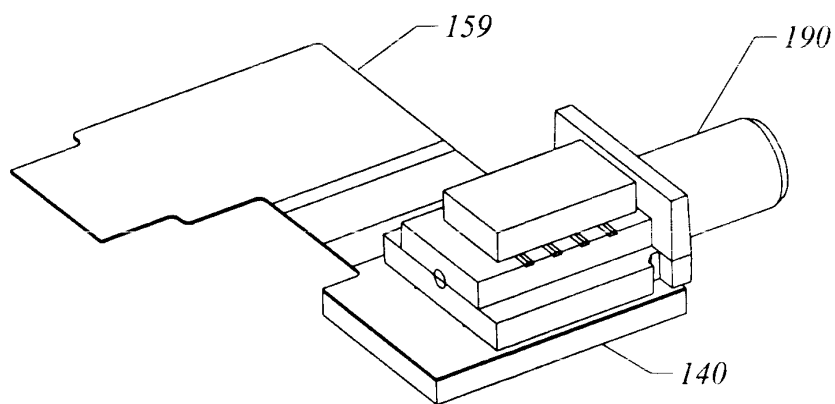


FIG. 17D

FIG. 18 is a schematic diagram of a circuit 15/15.

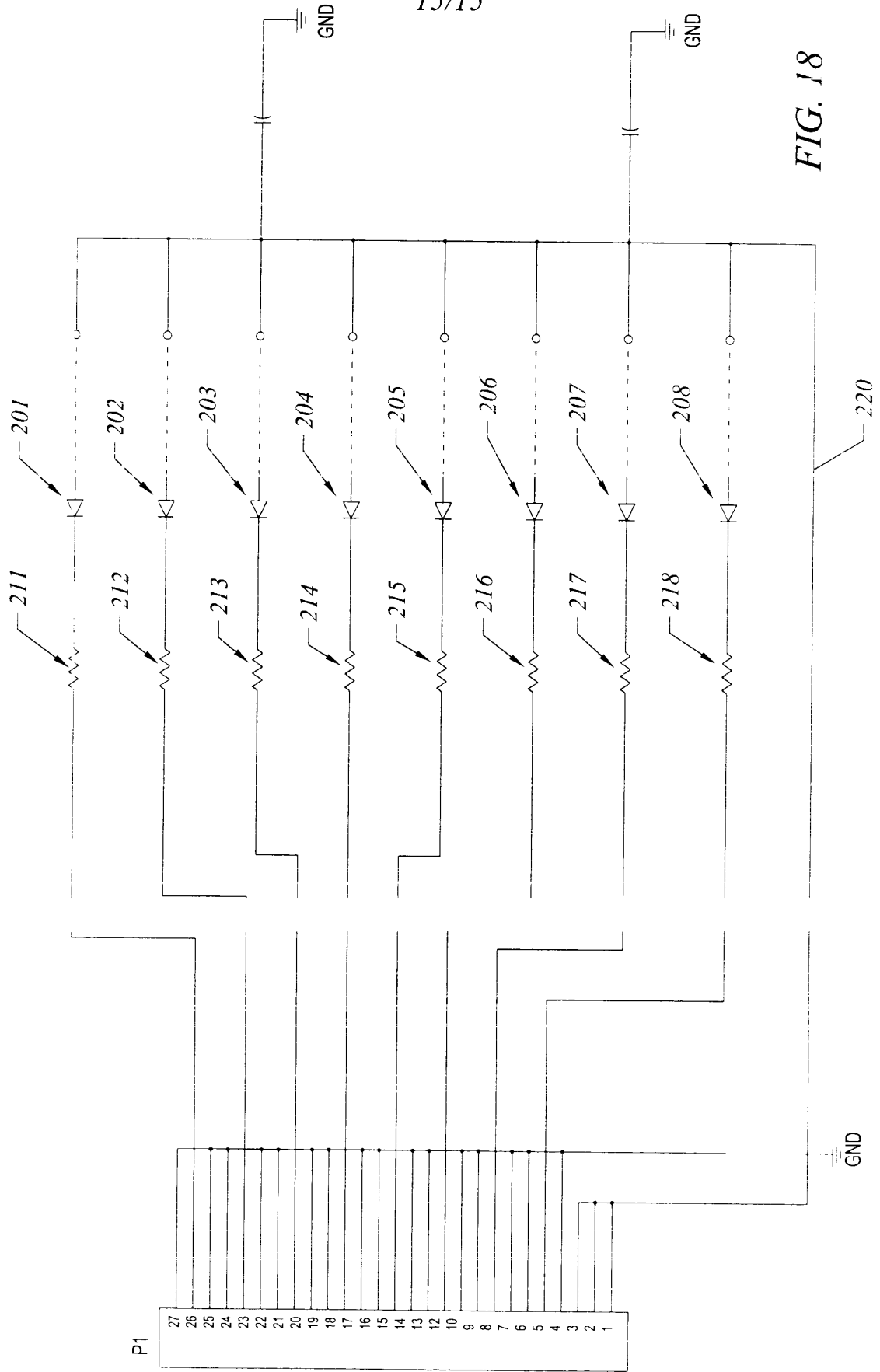


FIG. 18